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LADAS & PARRY LLP

Application Serial No. 10/779,415
Reply to office action of January 18, 2007

PATENT
Docket: CU-3570

REMARKS/ARGUMENTS

Reconsideration is respectfully requested.

Claims 1-8 were pending before the present amendment. By the present amendment, claims 1 and 3 were amended; and claims 6-8 are cancelled without prejudice. No new matter has been added.

In the office action (page 2), claims 1, 3, and 6 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 5,812,607 (Hutchinson) in view of U.S. Patent No. 4,468,790 (Hofelt). The "et al." suffix is omitted in a reference name.

Claim 6 has been cancelled without prejudice, by which the issues related to the rejection of claim 6 has been rendered moot.

As to claims 1 and 3, they have been amended to recite:

—wherein the digital conversion part of the transmitter includes a comparator and a flip-flop circuit, the comparator having a non-inverting input and a grounded inverting input, and

the digital conversion part performs the 1-bit quantization by inputting the modulated signal to the non-inverting input of the comparator, supplying an output of the comparator to the flip-flop circuit, and causing the flip-flop circuit to sample the output of the comparator at a rise time of a sampling clock signal supplied thereto.—

The amended claims 1 and 3 are not taught or suggested by Hutchinson or Hofelt, whether they are considered individually or in combination. The claimed digital conversion part (of the transmitter) includes, inter alia, a comparator and a flip-flop circuit where the comparator has a non-inverting input and a grounded inverting input. The claimed digital conversion part performs the 1-bit quantization by inputting the modulated signal to the non-inverting input of the comparator, supplying an output of the

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comparator to the flip-flop circuit, and causing the flip-flop circuit to sample the output of the comparator at a rise time of a sampling clock signal supplied thereto.

As to Hutchinson, Hutchinson fails to disclose the claimed –digital conversion part performing 1-bit quantization on a modulated signal– obtained as a result of the modulation in the modulation part (as the examiner has conceded this in the office action).

As to Hofelt, Hofelt col. 5, lines 31-33 (cited by the examiner) merely discloses:

“...having a feed-back one-bit quantizer QN’. The last system QN of the cascade will comprise in the receiver section RC the networks HN’ and 1/GN’ with parallel-”.

Thus, Hofelt col. 5, lines 31-33 neither discloses nor suggests the above added limitations of claims 1 and 3.

As Hutchinson and/or Hofelt (considered individually or in combination) fail to teach or suggest every limitation recited in claims 1 and 3, the applicants respectfully request allowance of claims 1 and 3.

In the office action (page 6), claims 2, 4-5, and 7-8 stand rejected under 35 U.S.C. §103(a) as being unpatentable over Hutchinson and Hofelt, and further in view of U.S. Patent No. 5,579,060 (Elberbaum).

Claims 7-8 have been cancelled without prejudice. Claims 2 and 4-5 are considered to be allowable at least since they depend from one of the independent claims 1 and 3, which are now considered to be in condition for allowance for the reasons above.

For the reasons set forth above, the applicants respectfully submit that claims 1-

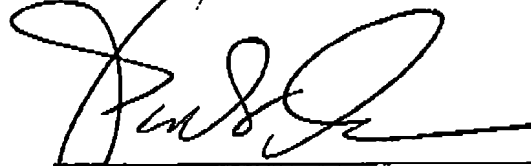
Application Serial No. 10/779,415
Reply to office action of January 18, 2007

APR 17 2007 PATENT
Docket: CU-3570

5, now pending in this application, are in condition for allowance over the cited references. Accordingly, the applicants respectfully request reconsideration and withdrawal of the outstanding rejections and earnestly solicit an indication of allowable subject matter.

This amendment is considered to be responsive to all points raised in the office action. Should the examiner have any remaining questions or concerns, the examiner is encouraged to contact the undersigned attorney by telephone to expeditiously resolve such concerns.

Respectfully submitted,



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